

(prior art)
Fig. 1

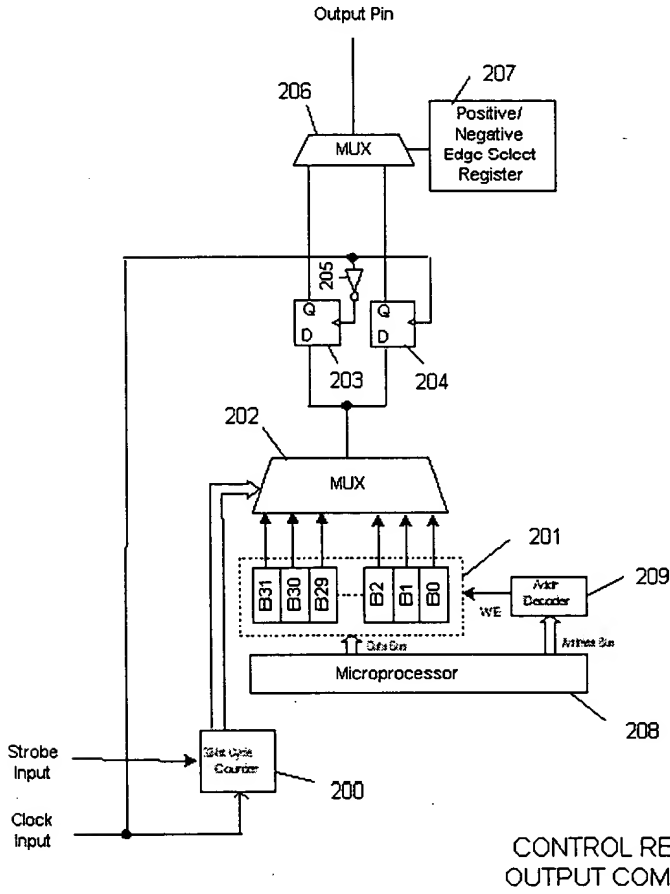
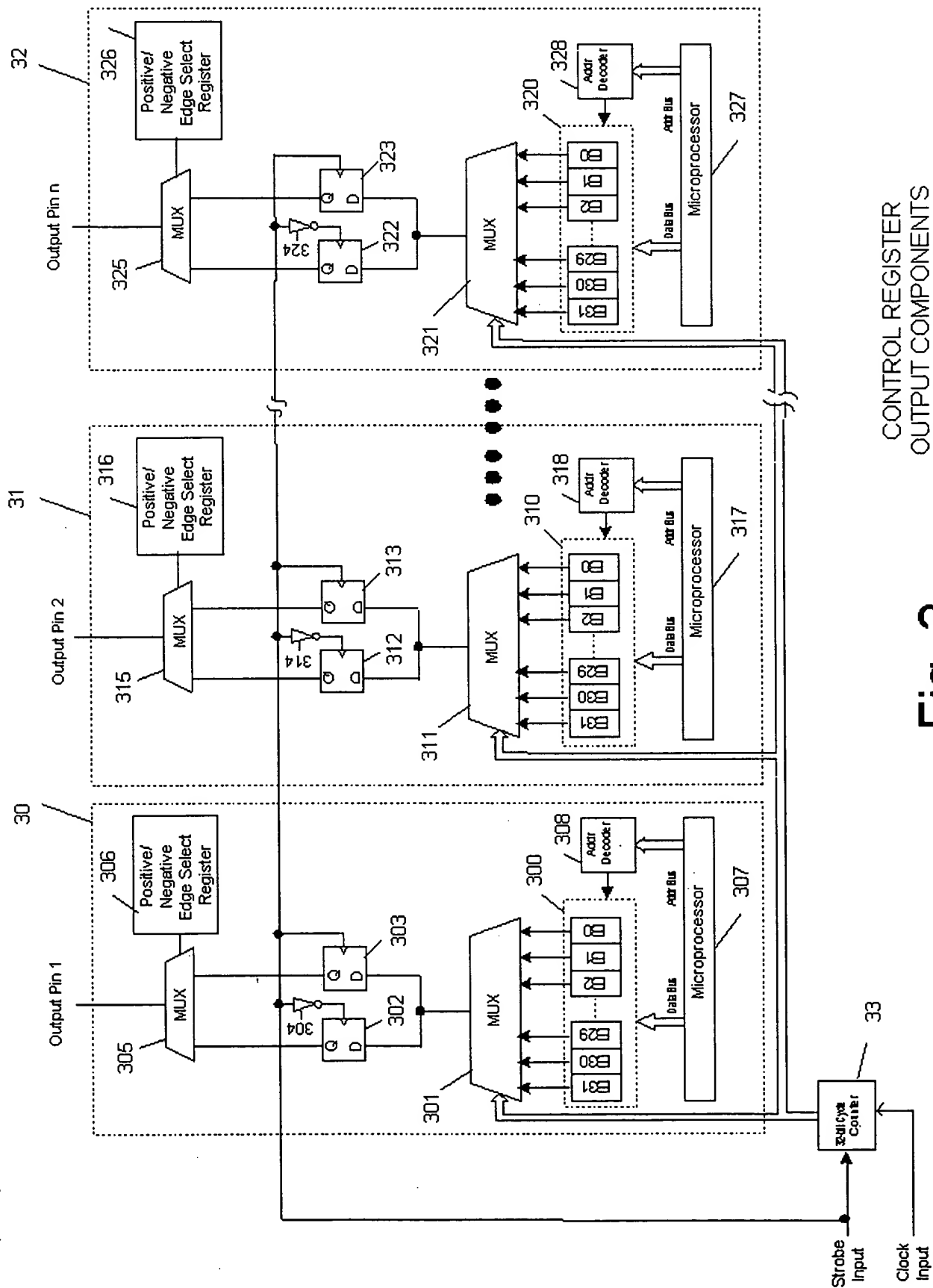


Fig. 2



CONTROL REGISTER
OUTPUT COMPONENTS
CONNECTED ON A BUS

Fig. 3

000027-567660

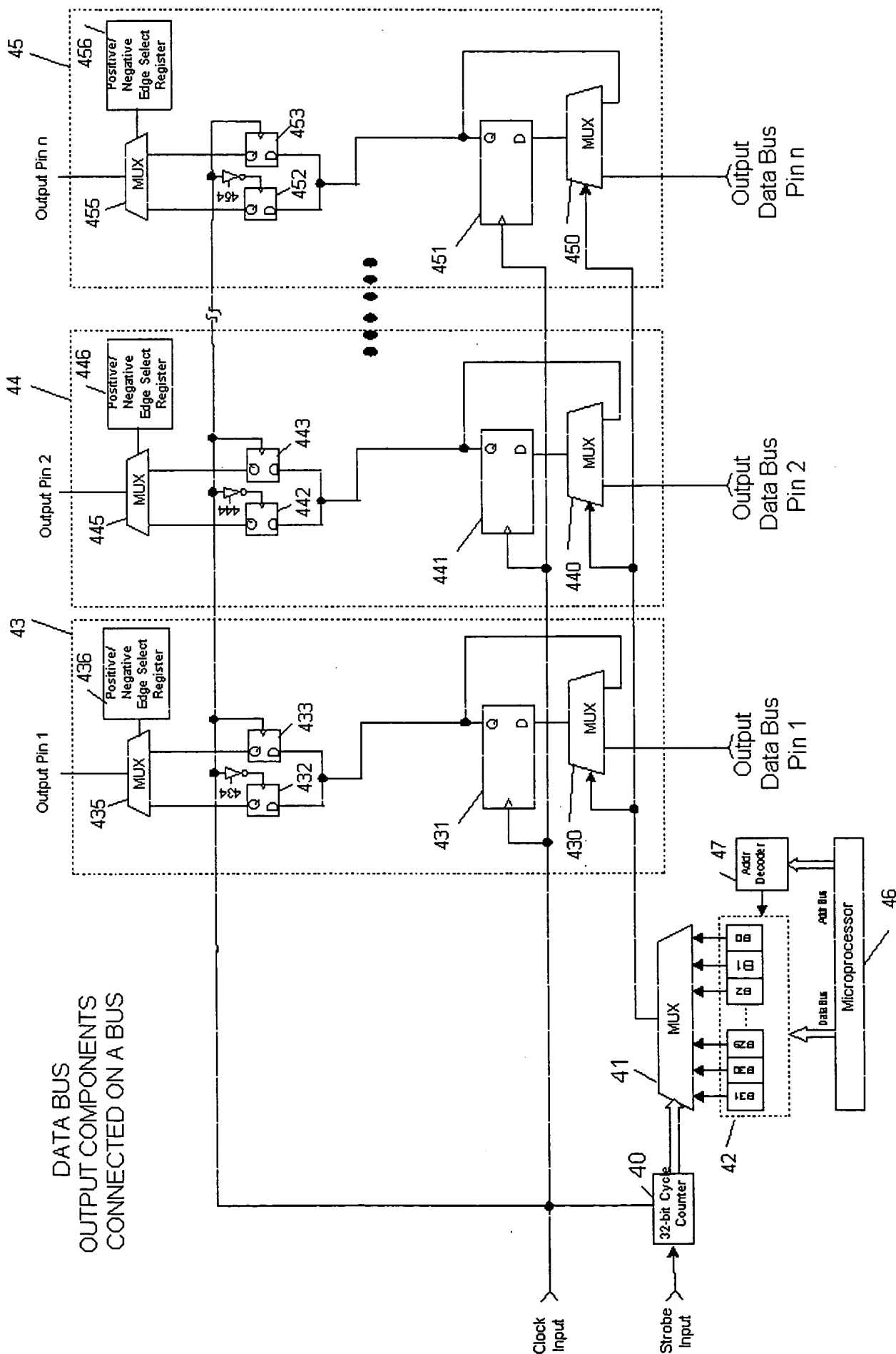


Fig. 4

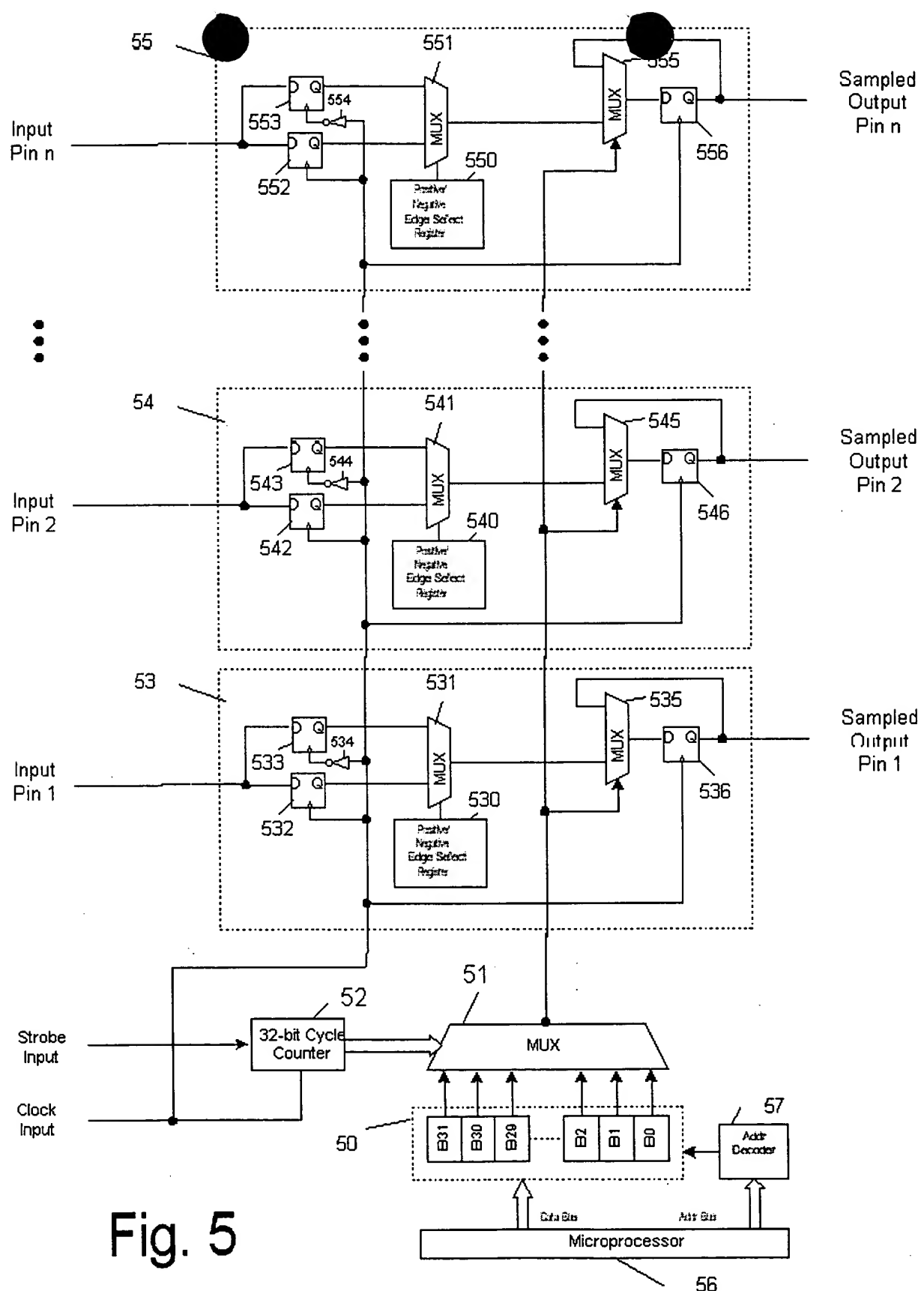


Fig. 5

INPUT COMPONENT

004031-5673200

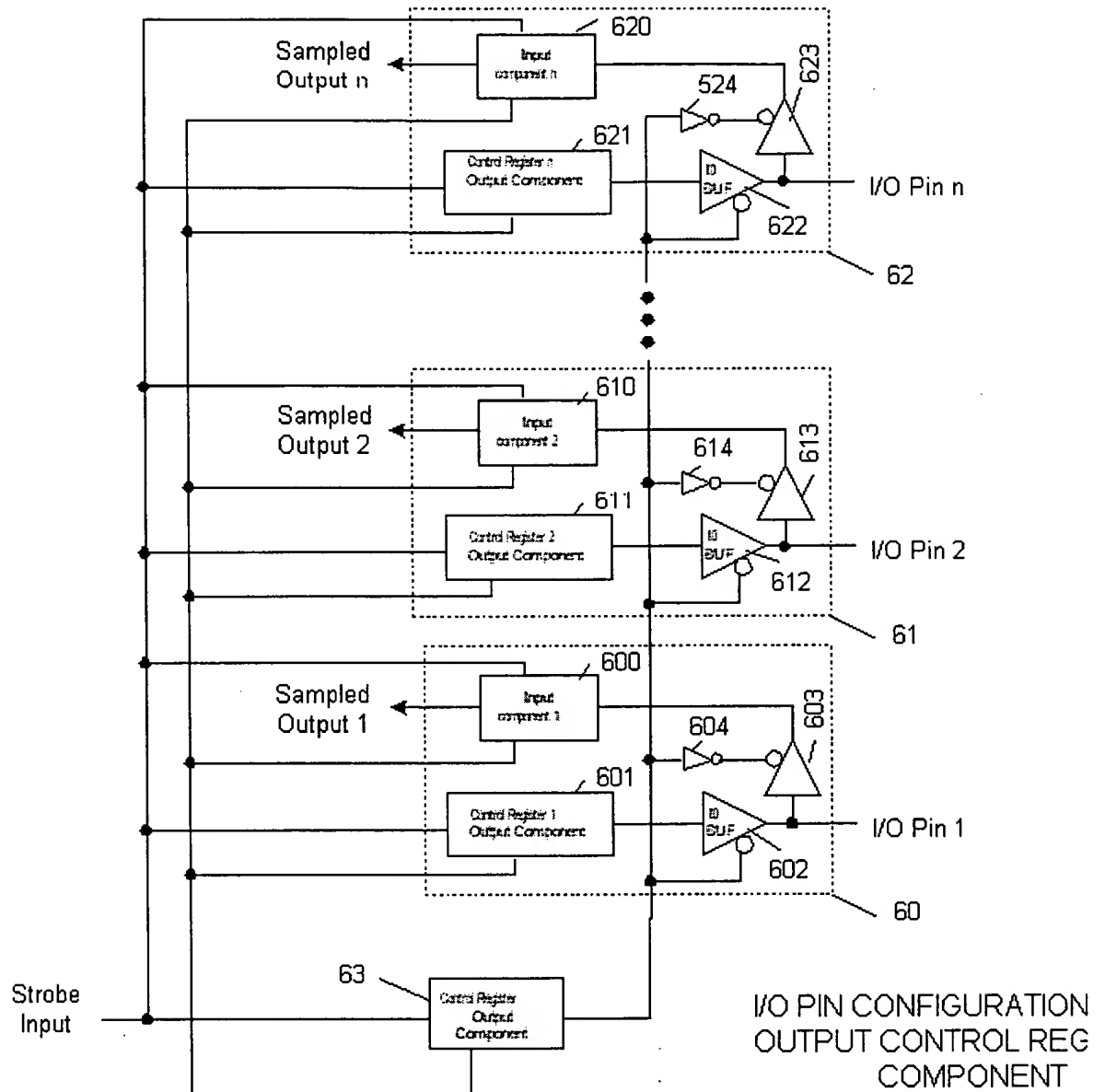
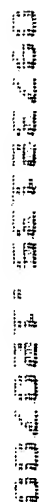


Fig. 6



I/O PIN CONFIGURATION FOR OUTPUT DATA BUS